IN THE UNITED STATES PATENT
AND TRADEMARK OFFICE

Applicants: Ngai et al.   )

Serial No.: **10/734,959**   )

Filed: December 12, 2003   )

Assignee: Intel Corporation   )

For: **"Methods and Apparatus to**
Compile Programs to Use Speculative
Parallel Threads"

Group Art Unit: Unknown   )

Examiner: Unknown   )

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I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on this date:

March 12, 2004

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

The publications listed on the enclosed PTO Form-1449 are submitted pursuant to

37 CFR §§ 1.56, 1.97, and 1.98. Copies of the patents or publications are enclosed.

TIME OF FILING

This information disclosure statement is being filed within three months of the filing date of the application. In accordance with 37 CFR §1.97(b), no certification or fee is required.
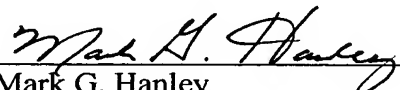
METHOD OF PAYMENT

No fee is required.

The Commissioner is authorized to charge any fee deficiency required by this paper, or credit any overpayment, to Deposit Account No. 50-2455. A copy of this paper is enclosed.

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Respectfully submitted,

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March 12, 2004   **Attorneys for Intel Corporation**

Form PTO-1449 (Modified)		U.S. Department of Commerce Patent and Trademark Office	Atty. Docket No. INTEL/17846		Serial No. 10/734,959
INFORMATION DISCLOSURE STATEMENT *(Use several sheets if necessary)*			Applicant **Ngai et al.**		
			Filing Date **December 12, 2003**	Group Art Unit **Unknown**	

(Stamp: OIPE MAR 1 5 2004 PATENT & TRADEMARK OFFICE JC48)
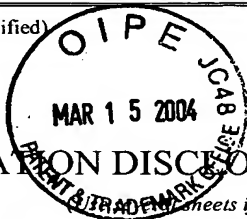
U.S. PATENT DOCUMENTS

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	INVENTOR(S)	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A01						

FOREIGN PATENT DOCUMENTS

*Examiner Initials		Document Number	Publication Date	Country	Class	Subclass	Translation Yes	No
	B01							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

	C01	Kim et al., *Compiling for Speculative Architectures*, Proceedings of the 12th International Workshop on Languages and Compilers for Parallel Computing [online] (August 1999). Retrieved from http://www.ece.purdue.edu/~eigenman/reports/lcpc99.ps.
	C02	Zahran et al., *Dynamic Thread Resizing for Speculative Multithreaded Processors*, International Conference on Computer Design [online] (October 2003). Retrieved from http://www.enee.umd.edu/~mzahran/publications.html.
	C03	Molina et al., *Trace-Level Speculative Multithreaded Architecture* [online] (no date). Retrieved from http://www.etse.urv.es/~cmolina/Home/Files/iccd02.pdf.

EXAMINER	DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance <u>and</u> not considered. Include copy of this form with next communication to applicant.